

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	53465	refresh	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/11/10 11:15
L2	4631	bank adj address	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/11/10 11:15
L3	2034	1 and 2	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/11/10 11:15
L4	3596	365/222.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/11/10 11:16
L5	268	3 and 4	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/11/10 11:16
L6	20	you-min-young.in.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/11/10 11:16

Day : Saturday  
Date: 11/10/2007


**PALM INTRANET**

Time: 11:15:09

## Inventor Name Search Result

Your Search was:

Last Name = YOU

First Name = MIN-YOUNG

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">09198521</a>	5986956	150	11/24/1998	REPAIR CONTROL CIRCUIT	YOU, MIN-YOUNG
<a href="#">09304643</a>	6111779	150	05/04/1999	CELL STRUCTURE FOR LOW ELECTRIC POWER STATIC RAM	YOU, MIN-YOUNG
<a href="#">09377163</a>	6128218	150	08/19/1999	HYBRID MEMORY DEVICE	YOU, MIN-YOUNG
<a href="#">09461413</a>	6359473	150	12/16/1999	AMPLIFIER FOR USE IN SEMICONDUCTOR INTEGRATED CIRCUITS	YOU, MIN-YOUNG
<a href="#">09562943</a>	6208581	150	05/02/2000	Hybrid memory device and method for controlling same	YOU, MIN-YOUNG
<a href="#">10061372</a>	6703871	150	02/04/2002	AMPLIFIER FOR USE IN SEMICONDUCTOR INTEGRATED CIRCUITS	YOU, MIN-YOUNG
<a href="#">10749350</a>	Not Issued	80	12/31/2003	Semiconductor memory device for reducing peak current during refresh operation	YOU, MIN-YOUNG
<a href="#">11647176</a>	Not Issued	30	12/29/2006	Semiconductor memory device with on die termination circuit	YOU, MIN-YOUNG
<a href="#">11647645</a>	Not Issued	30	12/29/2006	Semiconductor memory device capable of easily performing delay locking operation under high frequency system clock	YOU, MIN-YOUNG
<a href="#">11819811</a>	Not Issued	20	06/29/2007	Delay locked loop	YOU, MIN-YOUNG

Inventor Search Completed: No Records to Display.

**Search Another: Inventor**

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